

Notice of References Cited

Application/Control No.

10/759,819

Applicant(s)/Patent Under
Reexamination
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Li, Paul; "LVTC Logic family for live-insertion using standard CMOS process"; Pericom Semiconductor; January, 24, 2003; pages 1-3.
	V	Phillips Semiconductors; "The I2C-Bus Specification"; Phillips Semiconductors; Revision 2.1; January 2000; all pages.
	W	
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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